

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	"6769080".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/06 10:58
L2	2	"6158032".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/06 10:53
L3	20	"6158032".uref.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/06 10:53
L4	0	"6769080".uref.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/06 10:53
L5	2	"6766487".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/06 11:00
L6	1	"6766487".uref.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/06 10:58
L7	2	("6073254"   "6418545").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/06 10:59
L8	1	("6766487").URPN.	USPAT	OR	ON	2006/07/06 10:59
L9	3	"6615380".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/06 11:00

## EAST Search History

L10	2	"6615380".uref.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/06 11:00
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**Inventor Name Search Result**

Your Search was:

Last Name = CASWELL

First Name = AMANDA

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">10616846</a>	Not Issued	71	07/10/2003	Method and apparatus of reducing scan power in the process of unloading and restoring processor content by scan chain partition and disable	CASWELL, AMANDA C.
<a href="#">09978358</a>	<a href="#">6886106</a>	150	10/16/2001	A SYSTEM AND METHOD FOR CONTROLLING A MULTIPLEXER FOR SELECTING BETWEEN AN INPUT CLOCK AND AN INPUT DUTY-CYCLE-CORRECTED CLOCK AND OUTPUTTING THE SELECTED CLOCK AND AN ENABLE SIGNAL	CASWELL, AMANDA CHRISTINE

**Inventor Search Completed: No Records to Display.**

<b>Search Another: Inventor</b>	<b>Last Name</b>	<b>First Name</b>	<b>Search</b>
	<input type="text" value="CASWELL"/>	<input type="text" value="AMANDA"/>	

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Day : Thursday  
Date: 7/6/2006  
Time: 11:33:12


**PALM INTRANET**

## Inventor Name Search Result

Your Search was:

Last Name = PHAM

First Name = TUNG

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">06408278</a>	4513398	250	08/16/1982	BISTABLE LOGIC CIRCUIT USING FIELD EFFECT TRANSISTORS WITH A LOW VOLTAGE THRESHOLD AND STORAGE DEVICE INCORPORATING SUCH A CIRCUIT	PHAM NGU, TUNG
<a href="#">06871869</a>	4725566	150	06/09/1986	METHOD OF FORMING AT LEAST TWO METALLIZATIONS OF A SEMICONDUCTOR COMPONENT, COVERED WITH A DIELECTRIC LAYER	PHAM NGU, TUNG
<a href="#">07973309</a>	5434993	150	11/09/1992	METHODS AND APPARATUS FOR CREATING A PENDING WRITE-BACK CONTROLLER FOR A CACHE CONTROLLER ON A PACKET SWITCHED MEMORY BUS EMPLOYING DUAL DIRECTORIES	PHAM, TUNG
<a href="#">09668009</a>	Not Issued	161	09/21/2000	Dynamic foreign transaction generator	PHAM, TUNG
<a href="#">09692304</a>	Not Issued	161	10/19/2000	Dynamic queuing structure for a memory controller	PHAM, TUNG
<a href="#">10988208</a>	Not Issued	30	11/12/2004	Method of making solar cell contacts	PHAM, TUNG
<a href="#">11145538</a>	Not Issued	19	06/03/2005	Lead free solar cell contacts	PHAM, TUNG
<a href="#">11367940</a>	Not Issued	20	03/04/2006	Method and system for verifying equivalence of two representations of a stimulus pattern for testing a design	PHAM, TUNG N.
<a href="#">08004088</a>	5336627	150	01/13/1993	METHOD FOR THE MANUFACTURE OF A TRANSISTOR HAVING DIFFERENTIATED ACCESS REGIONS	PHAM, TUNG N.
<a href="#">09978357</a>	6931561	150	10/16/2001	APPARATUS AND METHOD FOR ASYNCHRONOUSLY INTERFACING HIGH-SPEED CLOCK DOMAIN AND LOW-SPEED CLOCK DOMAIN USING A PLURALITY OF STORAGE AND MULTIPLEXER COMPONENTS	PHAM, TUNG NGUYEN

<u>10616846</u>	Not Issued	71	07/10/2003	Method and apparatus of reducing scan power in the process of unloading and restoring processor content by scan chain partition and disable	PHAM, TUNG NGUYEN
<u>10870459</u>	Not Issued	30	06/17/2004	Multi-chip digital system signal identification method and apparatus	PHAM, TUNG NGUYEN
<u>11126120</u>	Not Issued	30	05/10/2005	Signal identification method and apparatus for analogue electrical systems	PHAM, TUNG NGUYEN

**Inventor Search Completed: No Records to Display.**

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**Inventor Name Search Result**

Your Search was:

Last Name = RUBIDOUX

First Name = TIMOTHY

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">10616846</a>	Not Issued	71	07/10/2003	Method and apparatus of reducing scan power in the process of unloading and restoring processor content by scan chain partition and disable	RUBIDOUX, TIMOTHY J.
<a href="#">09978358</a>	<a href="#">6886106</a>	150	10/16/2001	A SYSTEM AND METHOD FOR CONTROLLING A MULTIPLEXER FOR SELECTING BETWEEN AN INPUT CLOCK AND AN INPUT DUTY-CYCLE-CORRECTED CLOCK AND OUTPUTTING THE SELECTED CLOCK AND AN ENABLE SIGNAL	RUBIDOUX, TIMOTHY JOE

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